

Europäisches Patentamt  
European Patent Office  
Office européen des brevets



(11) EP 1 037 458 A1

(12)

# EUROPEAN PATENT APPLICATION

(43) Date of publication:  
20.09.2000 Bulletin 2000/38

(51) Int. Cl.<sup>7</sup>: H04N 3/15, H04N 5/217

(21) Application number: 00301689.6

(22) Date of filing: 02.03.2000

(84) Designated Contracting States:  
AT BE CH CY DE DK ES FI FR GB GR IE IT LI LU  
MC NL PT SE  
Designated Extension States:  
AL LT LV MK RO SI

(72) Inventor:  
Lin, Denny M.,  
c/o Canon Information Systems, Inc.  
Irvine, California 92612 (US)

(30) Priority: 15.03.1999 US 267781

(74) Representative:  
Beresford, Keith Denis Lewis et al  
BERESFORD & Co.  
High Holborn  
2-5 Warwick Court  
London WC1R 5DJ (GB)

(71) Applicant:  
CANON KABUSHIKI KAISHA  
Tokyo (JP)

## (54) Reducing discontinuities in segmented imaging sensors

(57) A segmented imaging sensor having plural output pipelines in correspondence to the number of segments. Discontinuities between segments of the electronic imaging sensor are reduced by providing duplicated pixel values for pixels in an overlap region between segments, and by providing the duplicated pixels to processors that process the output from pipelines whose segments border on the overlap region.

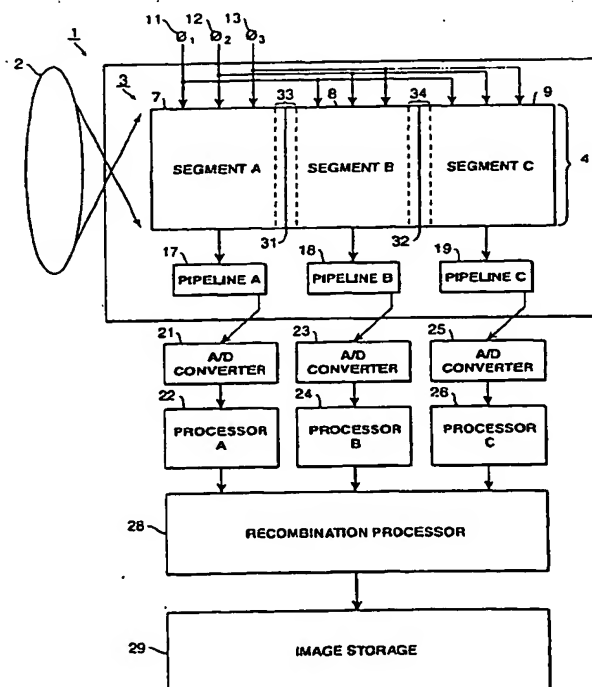


FIG. 1

EP 1 037 458 A1

## Description

[0001] The present invention relates to electronic imaging sensors, such as charge couple devices (CCDs), that are segmented into plural segments with a respective one of plural output pipelines for each segment. In particular, the invention pertains to duplicating image data for an overlap region that includes the discontinuity between individual segments of the sensors, with the duplicated data being provided to each of the output pipelines for segments that border the overlap region.

[0002] Recent advances in manufacturing processes for electronic imaging sensors such as CCDs have resulted in electronic imaging sensors having many millions of pixels arranged in a two-dimensional array. Because of the large number of pixels, however, there has been an undesirable increase in the processing time needed merely to shift the data from the electronic imaging sensor.

[0003] Several proposals have been considered to address the increase in shift out time. According to one such proposal, the electronic imaging sensor is segmented into plural different disjoint areas, such as being divided into left and right halves. Each segment of the electronic imaging sensor is provided with its own output pipeline. As a consequence of this arrangement, it is possible to reduce the shift out time in proportion to the number of segments. For example, when an electronic imaging sensor is divided into mutually disjoint left and right halves, it is possible to reduce the shift out time by a factor of two.

[0004] One problem encountered in use of a segmented electronic imaging sensor is the discontinuity in image data formed at boundaries between each of the segments. If data shifted from each segment is immediately re-combined prior to any processing, the effects of such discontinuities can be minimized since image data for the entire image is available to the processor. However, it is becoming increasingly commonplace to provide a separate processor for each output pipeline. These processors work independently of each other, on the segment of electronic imaging sensor data corresponding to its output pipeline. Thus, when an image is formed from the re-combined image data processed individually by each processor, discontinuities at the sensor segment are readily apparent.

[0005] An aspect of the invention reduces discontinuities at each segment of a segmented electronic imaging sensor by duplicating image data for an overlap region that includes the discontinuity between individual segments, and by providing the duplicated image data to the output pipeline of each segment of the electronic imaging sensor that borders the overlap region.

[0006] Thus, in one aspect, the invention is an electronic imaging sensor segmented into plural different disjoint segments, with each segment provided with a processor. Means are provided to duplicate pixel values

for an overlap region that includes a segment boundary, with the duplicated pixel values being provided to the processors for the segments that border on the overlap region.

[0007] Because pixel data for an overlap region at segment boundaries is duplicated to each of the different processors, those processors are able to compensate more accurately for image discontinuities. For example one common post-processing function on electronic imaging sensor data is to perform white balance correction whereby each processor adjusts the red, green and blue components of a color image so as to achieve an overall white balance. Because the present invention provides duplicated image data in overlap regions of each segment, individual processors are able to achieve an overall more pleasing white balance correction, with reduced discontinuities between image segments.

[0008] Preferably, the overlap region is more than one pixel wide, such as six or eight pixels wide, and preferably centered on the boundary between segments of the segmented electronic imaging sensor. Three different arrangements are currently preferred for providing such duplicated image data.

[0009] According to a first arrangement, circuitry is included directly on the sensor chip to include a charge (or voltage) duplication circuit for pixels in the overlap region. The duplicated pixels are thereafter shifted out of the electronic imaging sensor in ordinary course to respective ones of the output pipelines.

[0010] According to a second arrangement for duplicating pixels for an overlap region, some circuitry is included directly on the sensor chip itself, and other circuitry is provided off chip. The circuitry provided on the chip itself includes a separate shift-out signal line for pixels in the overlap region. This separate shift-out line is different from the output pipelines for the sensor segments. Pixels in the overlap region are stored in an intermediate buffer, preferably provided off-chip. It is the intermediate buffer that provides duplicate pixel values to each of the image processors associated with the output pipelines for the sensor segments.

[0011] According to a third arrangement for providing duplicate pixels, all circuitry is provided off-chip. The circuitry includes a communication link between the processors associated with respective ones of the output pipelines. The communication link allows the processors to share duplicate pixel image data from the overlap region.

[0012] Embodiments of the present invention will now be described with reference to the accompanying drawings, in which:

Figure 1 is a representational view of an image storage device that can utilize an electronic imaging sensor according to an embodiment of the invention.

Figure 2 is a representational view of a first embod-

iment of an electronic imaging sensor according to the invention, in which image data for an overlap region is duplicated by circuitry directly on a chip containing the electronic imaging sensor.

Figure 3A is a schematic of charge duplicating circuitry for an overlap region of a sensor segment.

Figure 3B is a schematic of charge circuitry for a non-overlap region of a sensor segment.

Figure 4 is a flowchart for explaining generation of image data for an image formed on a sensor array, in which image data is duplicated for an overlap region at a boundary between two mutually disjoint segments of a sensor array according to a first embodiment of the invention.

Figure 5 is a representational view of a second embodiment of an electronic imaging sensor according to the invention, in which image data for an overlap region is stored to an intermediate buffer.

Figure 6 is a flowchart for explaining generation of image data for an image formed on a sensor array, in which image data is duplicated for an overlap region at a boundary between two mutually disjoint segments of a sensor array according to a second embodiment of the invention.

Figure 7 is a representational view of a third embodiment of an electronic imaging sensor according to the invention, in which pixel data for an overlap region is communicated off-chip between processors.

Figure 8 is a flowchart for explaining generation of image data for an image formed on a sensor array, in which image data is duplicated for an overlap region at a boundary between two mutually disjoint segments of a sensor array according to a third embodiment of the invention.

[0013] Figure 1 is a representational view of an image storage device that can utilize an electronic imaging sensor according to an embodiment of the invention. Shown in Figure 1 is electronic imaging sensor chip 1, such as a charge couple device (CCD) chip, along with lens system 2 for focussing incident light for an image onto sensor array 3 in unmasked area 4 of sensor chip 1. Sensor array 3 includes a plurality of mutually disjoint sensor segments. As shown in Figure 1, these sensor segments include segment A 7, segment B 8 and segment C 9.

[0014] Each sensor segment preferably includes a plurality of rows of photosensitive cells. Each cell preferably corresponds to a pixel of the image data generated by the sensor chip. When light strikes one of these cells, the light generates a charge or voltage in the cell for the corresponding pixel.

[0015] After the light has generated these charges or voltages for a predetermined period of time corresponding to a single image, the charges or voltages are shifted out of each sensor segment so as to provide

image data for the image focussed onto sensor array 3. Preferably, the charges or voltages are shifted out of each segment row-by-row. In addition, the sensor segments are preferably shuttered, either physically or electronically, so that no more charges or voltages are generated by incident light during the shifting operation. This shuttering tends to prevent or diminish image smearing that could occur from charges generated by incident light as the charges or voltages are shifted out of the sensor segments.

[0016] Preferably, phased clocks  $\phi_1$  11,  $\phi_2$  12 and  $\phi_3$  13 are provided for shifting the charges or voltages out of each sensor segment using techniques that are known in the art. Each of the phased clocks preferably has a 1/3 duty cycle and is offset from the other clocks. By successively clocking cells in a sensor segment using these clocks, the charges or voltages are shifted out of the segment. Preferably, the charges or voltages are shifted one row at a time into the pipelines corresponding to each of the sensor segments. Thus, rows of charges or voltages are shifted one by one from segment A 7 to pipeline A 17, from segment B 8 to pipeline B 18, and from segment C 9 to pipeline C 19.

[0017] After a row of charges or voltages is shifted into a pipeline, the charges or voltages for that row are serially output to an image processor, preferably through an A/D converter which converts the charges or voltages into digital pixel data. The image processor processes the pixel data, for example to perform white balance correction.

[0018] Thus, charges or voltages are serially output from pipeline A 17 to A/D converter 21 for conversion to digital pixel data, and the data is sent to processor A 22 for processing. Charges or voltages are serially output from pipeline B 18 to A/D converter 23 for conversion to digital pixel data, and the data is sent to processor B 24 for processing. Likewise, charges or voltages are serially output from pipeline C 19 to A/D converter 25 for conversion to digital pixel data, and the data is sent to processor C 26 for processing.

[0019] Next, another row of charges or voltages is shifted from the sensor segments to the pipelines, and the process is repeated until pixel data has been generated for all rows of the sensor segments. Recombination processor 28 then combines the pixel data from processor A 22, processor B 24 and processor C 26. Alternatively, recombination processor 28 processes the pixel data as it is generated by processor A 22, processor B 24 and processor C 26. The image data from recombination processor 28 preferably is stored in image storage 29.

[0020] After an entire image has been processed, sensor array 3 preferably is unshuttered (either physically or electronically), and another image is processed.

[0021] According to the invention, duplicate pixel values are generated for overlap regions that include boundaries between the sensor segments. Preferably, each overlap region includes cells corresponding to a

width of a few pixels, such as cells for six or eight pixels, on each side of the corresponding boundary.

[0022] In Figure 1, the boundaries between segments are boundary 31 between segment A 7 and segment B 8, and boundary 32 between segment B 8 and segment C 9. The corresponding overlap regions are overlap region 33 and overlap region 34, respectively.

[0023] Processors A 22, B 24 and C 26 compensate for discontinuities between the sensor segments using the duplicate pixel values. For example, when the processors correct white balance for their respective sensor segments, the processors use the duplicate pixel values for the overlap regions in the white balance calculations. As a result, a more pleasing white balance is achieved that tends to minimize discontinuities between image data for the sensor segments.

[0024] Three different embodiments of the invention for generating the duplicate pixel values are described hereinbelow. While these three embodiments are described in detail, the invention is not limited to these embodiments.

#### [First Embodiment]

[0025] Figure 2 is a representational view of a first embodiment of an electronic imaging sensor according to the invention.

[0026] Briefly, according to the first embodiment, an electronic imaging sensor includes a sensor array segmented into plural disjoint segments, a respective plurality of output pipelines, each of which corresponds to each of the plural segments of the sensor array, and circuitry for duplicating image data for an overlap region at each boundary between segments. The circuitry includes charge or voltage duplicating circuitry that obtains multiple outputs for each pixel in the overlap region. The duplication circuitry provides each of the multiple outputs to individual ones of the output pipelines that border on the overlap region.

[0027] In more detail, Figure 2 shows electronic imaging sensor chip 31 having sensor array 33, which includes mutually disjoint sensor segments A, B and C, designated at numerals 37, 38 and 39. Each of these sensor segments outputs rows of charges or voltages for an image focussed thereon in accordance with phased clocks  $\phi_1$  41,  $\phi_2$  42 and  $\phi_3$  43, as explained above with reference to Figure 1.

[0028] According to the first embodiment of the invention, image data for overlap regions is duplicated by circuitry that duplicates the charges or voltages for the overlap regions. Preferably, this circuitry is provided on sensor chip 31.

[0029] In Figure 2, circuitry 51 duplicates the charges or voltages for overlap region 52, and circuitry 53 duplicates the charges or voltages for overlap region 54. One possible structure for each of circuitry 51 and 52 is explained below with reference to Figure 3A.

[0030] Because the voltages or charges outside of

the overlap regions are not duplicated, circuitry 56, 57 and 58 provide the charges or voltages from non-overlap regions outside of the overlap regions for each of the sensor segments to the respective pipelines without duplication. Thus, circuitry 56 provides charges or voltages from non-overlap region 61 to pipeline A 62, circuitry 57 provides charges or voltages from non-overlap region 64 to pipeline B 65, and circuitry 58 provides charges or voltages from non-overlap region 67 to pipeline C 68. One possible structure for each of circuitry 56, 57 and 58 is explained below with reference to Figure 3B.

[0031] Each of the pipelines is connected to a respective one of processor A 71, processor B 72 and processor C 73. Pipeline A 62 is preferably connected to processor A 71 through A/D converter 74; pipeline B 65 is preferably connected to processor B 72 through A/D converter 75; and pipeline C 68 is preferably connected to processor C 73 through A/D converter 76.

[0032] Figure 3A is a schematic of charge duplicating circuitry for an overlap region of a sensor segment. As shown in Figure 3A, charge 81 is input from an overlap region. Charge 81 travels down both of branches 82 and 83. Branch 82 is provided with resistor 85 and capacitor 86, and branch 83 is provided with resistor 87 and capacitor 88. These resistors and capacitors are preferably balanced so as to produce duplicate output charges 89 and 90 from input charge 81. Unshown circuitry compensates for any needed amplification, so as to compensate for attenuation by the resistor-capacitor network.

[0033] Figure 3B is a schematic of charge circuitry for a non-overlap region of a sensor segment. This circuitry does not duplicate input charge 91 from a non-overlap region. Thus, the circuitry in Figure 3B has one branch 92 with resistor 93 and capacitor 94, which operate to produce output charge 95 from input charge 91.

[0034] Figure 4 is a flowchart for explaining generation of image data from charges for an image formed on a sensor array, in which image data is duplicated for an overlap region at a boundary between two mutually disjoint segments of a sensor array according to the first embodiment.

[0035] Briefly, an electronic imaging sensor has a sensor array with plural mutually disjoint segments. The plural segments include at least a first segment and a second segment separated by a boundary. Charges or voltages from a non-overlap region of the first segment are stored into a pipeline. Charges or voltages from an overlap region of the first segment and the second segment also are stored into the pipeline. The charges or voltages stored in the pipeline are then provided to a processor.

[0036] In more detail, charges or voltages from a non-overlap region of a row of a sensor segment are stored to a pipeline in step S401. Charges or voltages from non-overlap region 61 are stored to pipeline A 62, from non-overlap region 64 are stored to pipeline B 65,

and from non-overlap region 67 are stored to pipeline C 68.

[0037] In step S402, charges or voltages from an overlap region of a row of a segment and an adjacent segment are duplicated by duplicating circuitry, and the duplicated charges or voltages are stored to the pipelines for those segments. Charges or voltages from overlap region 52 are duplicated by circuitry 51 and stored to pipelines A 62 and B 65, and charges or voltages from overlap region 54 are duplicated by circuitry 53 and stored to pipelines B 65 and C 68.

[0038] In step S403, charges or voltages from the pipelines are processed by the respective processors. Charges or voltages from pipeline A 62 are processed by processor A 71 after conversion to digital pixel data by A/D converter 74. Charges or voltages from pipeline B 65 are processed by processor B 72 after conversion to digital pixel data by A/D converter 75, and charges or voltages from pipeline C 68 are processed by processor C 73 after conversion to digital pixel data by A/D converter 76.

[0039] In step S404, if more rows need to be processed to complete processing of the image, flow returns to step S401. Otherwise, flow proceeds to step S405, where image data from the processors is recombined so as to form a whole image corresponding to an image focussed onto sensor array 33.

[0040] By virtue of the foregoing, each of the processors for a segment receives image data for pixels in an overlap region or regions of an adjacent segment or segments. As a result, the processors corresponding to each of the segments are able to compensate for image discontinuities between the segments. For example, white balance correction for adjusting red, green and blue color components of a color image can be more readily performed in each of processors 71, 72 and 73 so as to achieve an overall white balance.

#### [Second Embodiment]

[0041] Figure 5 is a representational view of a second embodiment of an electronic imaging sensor according to the invention.

[0042] Briefly, according to the second embodiment, an electronic imaging sensor includes a sensor array segmented into plural mutually disjoint segments, a respective plurality of output pipelines, each of which corresponds to each of the plural segments of the sensor array, and a respective plurality of processors. Each processor is coupled to a respective one of the output pipelines. The electronic imaging sensor also includes an output pipeline, or shift out line, for outputting pixel values of pixels in the overlap region to an intermediate buffer. The intermediate buffer provides duplicate pixel values to each processor whose segment borders the overlap region.

[0043] In more detail, Figure 5 shows electronic imaging sensor chip 101 having sensor array 103,

which includes sensor segment A 107, sensor segment B 108 and sensor segment C 109. Each of these sensor segments outputs rows of charges or voltages for an image focussed thereon in accordance with phased clocks  $\phi_1$  111,  $\phi_2$  112 and  $\phi_3$  113, as explained above with reference to Figure 1.

[0044] According to the second embodiment of the invention, image data for overlap regions is duplicated by storing charges or voltages for overlap regions to shift out lines, and then to intermediate buffers that supply the charges to the processors for the segments for the overlap regions. Preferably, the shift out lines are provided on sensor chip 101, while the intermediate buffers are provided off sensor chip 101.

[0045] In Figure 5, charges or voltages from non-overlap region 115 are stored to pipeline A 116, which preferably supplies the charges or voltages to processor A 117 through A/D converter 118. Likewise, charges or voltages from non-overlap region 119 are stored to pipeline B 120, which preferably supplies the charges or voltages to processor B 121 through A/D converter 122; and charges or voltages from non-overlap region 123 are stored to pipeline C 124, which preferably supplies the charges or voltages to processor C 125 through A/D converter 126.

[0046] Charges or voltages from overlap region 131 are sent to shift out line 132 and are stored to intermediate A/B buffer 133. This buffer supplies the charges or voltages to processor A 117 and processor B 121, preferably through A/D converter 118 and A/D converter 122 respectively. Likewise, charges or voltages from overlap region 135 are sent to shift out line 136 and are stored to intermediate B/C buffer 137. This buffer supplies the charges or voltages to processor B 121 and processor C 125, preferably through A/D converter 122 and A/D converter 126 respectively.

[0047] Figure 6 is a flowchart for explaining generation of image data from charges for an image formed on a sensor array, in which image data is duplicated for an overlap region at a boundary between two mutually disjoint segments of a sensor array according to the second embodiment.

[0048] Briefly, an electronic imaging sensor has a sensor array with plural disjoint segments. The plural segments include at least a first segment and a second segment separated by a boundary. Charges or voltages from a non-overlap region of the first segment are stored into a pipeline. Charges or voltages from the overlap region of the first segment and the second segment are sent to a shift out line. The charges or voltages from the shift out line are stored to an intermediate buffer. The charges or voltages are provided from the pipeline and from the intermediate buffer to a processor.

[0049] In more detail, charges or voltages from a non-overlap region of a row of a sensor segment are stored to a pipeline in step S601. Charges or voltages from non-overlap region 115 are stored to pipeline A 116, from non-overlap region 119 are stored to pipeline

B 120, and from non-overlap region 123 are stored to pipeline C 124.

[0050] In step S602, charges or voltages from an overlap region of a row of a segment and an adjacent segment are sent to a shift out line. Charges or voltages from overlap region 131 are sent to shift out line 132, and charges or voltages from overlap region 135 are sent to shift out line 136.

[0051] The charges or voltages from the shift out lines are stored to intermediate buffers in step S603. Intermediate A/B buffer 133 stores the charges or voltages from shift out line 132, and intermediate B/C buffer 137 stores the charges or voltages from shift out line 136.

[0052] In step S604, charges or voltages from the pipelines and the intermediate buffers are sent to respective processors for processing, preferably through A/D converters. Charges or voltages from pipeline A 116 and intermediate A/B buffer 133 preferably are sent through A/D converter 118 to be processed by processor A 117; charges or voltages from pipeline B 120, intermediate A/B buffer 133 and intermediate B/C buffer 137 preferably are sent through A/D converter 122 to be processed by processor B 121; and charges or voltages from pipeline C 124 and intermediate B/C buffer 137 preferably are sent through A/D converter 126 to be processed by processor C 125. Thus, in the second embodiment, data for an overlap region is duplicated in step S604 by being sent from a corresponding intermediate buffer to processors for segments that border on the overlap region.

[0053] In step S605, if more rows need to be processed to complete processing of the image, flow returns to step S601. Otherwise, flow proceeds to step S606, where image data from the processors is recombined so as to form a whole image corresponding to an image focussed onto sensor array 103.

[0054] By virtue of the foregoing, each of the processors for a segment receives image data for pixels in an overlap region or regions of an adjacent segment or segments. As a result, the processors corresponding to each of the segments are able to compensate for image discontinuities between the segments.

#### [Third Embodiment]

[0055] Figure 7 is a representational view of a third embodiment of an electronic imaging sensor according to the invention.

[0056] Briefly, according to the third embodiment, an electronic imaging sensor includes a sensor array segmented into plural mutually disjoint segments, a respective plurality of output pipelines, each of which corresponds to each of the plural segments of the sensor array, and a respective plurality of processors. Each processor is coupled to a respective one of the output pipelines. A communication link is provided between processors that border an overlap region, and duplicate

pixels are communicated between the processors over the communication link.

[0057] In more detail, Figure 7 shows electronic imaging sensor chip 141 having sensor array 143, which includes sensor segment A 147, sensor segment B 148 and sensor segment C 149. Each of these sensor segments outputs rows of charges or voltages for an image focussed thereon in accordance with phased clocks  $\phi_1$  151,  $\phi_2$  152 and  $\phi_3$  153, as explained above with reference to Figure 1.

[0058] According to the third embodiment of the invention, image data for overlap regions is duplicated by communicating the image data over communication lines between processors for the respective image regions. Preferably, the processors and the communication lines are provided off-chip.

[0059] In Figure 7, charges or voltages from segment A 147, including non-overlap region 157 and a part of overlap region 158 in segment A 147, are stored to pipeline A 160. Likewise, charges or voltages from segment B 148, including non-overlap region 162 and parts of overlap regions 158 and 164 in segment B 148, are stored to pipeline B 166; and charges or voltages from segment C 149, including non-overlap region 168 and a part of overlap region 164 in segment C 149, are stored to pipeline C 170.

[0060] Each of the pipelines provides the charges or voltages to respective ones of processors A 173, B 174 and C 175, preferably through A/D converters 176, 177 and 178. These processors communicate via communication lines 180 and 181 so as to share overlap pixel data for overlap regions shared by the corresponding sensor segments. Thus, in the preferred embodiment, processors A 173 and B 174 communicate pixel data for overlap region 158 over communication line 180, and processors B 174 and C 175 communicate pixel data for overlap region 164 over communication line 181.

[0061] Figure 8 is a flowchart for explaining generation of image data from charges for an image formed on a sensor array, in which image data is duplicated for an overlap region at a boundary between two mutually disjoint segments of a sensor array according to the second embodiment.

[0062] Briefly, an electronic imaging sensor has a sensor array with plural disjoint segments. The plural segments include at least a first segment and a second segment separated by a boundary. Charges or voltages from the first segment are stored into a pipeline. The charges or voltages are provided from the pipeline to a first processor for processing pixel data for the first segment. Pixel data for the overlap region is communicated between the first processor and a second processor, wherein the second processor is for processing pixel data for the second segment.

[0063] In more detail, charges or voltages from an entire row of a sensor segment are stored to a pipeline in step S801. Charges or voltages from sensor segment

A 147 are stored to pipeline A 160, from sensor segment B 148 are stored to pipeline B 166, and from sensor segment C 149 are stored to pipeline C 170.

[0064] In step S802, charges or voltages from the pipelines are processed by respective ones of the processors, along with overlap data provided from processors for adjacent sensor segments. Processor A 173 processes charges or voltages from pipeline A 160, preferably received through A/D converter 176, along with overlap data from processor B 174 for a part of overlap region 158 in segment B 148. Processor B 174 processes charges or voltages from pipeline B 166, preferably received through A/D converter 177, along with overlap data from processor A 173 for a part of overlap region 158 in segment A 147 and overlap data from processor C 175 for a part of overlap region 164 in segment C 149. Processor C 175 processes charges or voltages from pipeline C 170, preferably received through A/D converter 178, along with overlap data from processor B 174 for a part of overlap region 164 in segment B 148. The overlap data is communicated between the processors over communication lines 180 and 181.

[0065] In step S803, if more rows need to be processed to complete processing of the image, flow returns to step S801. Otherwise, flow proceeds to step S804, where image data from the processors is recombined so as to form a whole image corresponding to an image focussed onto sensor array 143.

[0066] By virtue of the foregoing, each of the processors for a segment receives image data for pixels in an overlap region or regions of an adjacent segment or segments. As a result, the processors corresponding to each of the segments are able to compensate for image discontinuities between the segments.

#### [Variations]

[0067] Instead of receiving data through A/D converters, the processors can be capable of directly processing the charges or voltages. For example, each processor can include an embedded A/D converter or other circuitry for performing image processing on the charges or voltages. In that case, the A/D converters shown in Figures 1, 2, 5 and 7 can be omitted.

[0068] It is possible to combine any of the features of each of the embodiments described above. In addition, an electronic imaging sensor chip could include a sensor array that has more or less sensor segments than the three segments discussed herein, arranged vertically, horizontally, in a grid, or in some other fashion.

[0069] Accordingly, while the invention is described above with respect to what is currently considered its preferred embodiments, it is to be understood that the invention is not limited to that described above. To the contrary, the invention is intended to cover various modifications and equivalent arrangements included within

the scope of the appended claims.

#### Claims

1. An imaging sensor which includes:

a sensor array segmented into plural disjoint segments;  
a respective plurality of output pipelines, one of said output pipelines corresponding to each of said plural segments of the sensor array; and  
means for duplicating image data for an overlap region at each boundary between segments.

2. An imaging sensor according to claim 1, wherein said means for duplicating image data comprises charge or voltage duplicating circuitry that obtains multiple outputs for each pixel in the overlap region, and wherein said duplication circuitry provides each of the multiple outputs to individual ones of said output pipelines that border on the overlap region.

3. An imaging sensor according to claim 1, further comprising a respective plurality of processors, each processor coupled to a respective one of the output pipelines, and wherein said means for duplicating includes an output pipeline for outputting pixel values of pixels in the overlap region to an intermediate buffer, the intermediate buffer providing duplicate pixel values to each processor whose segment borders the overlap region.

4. An imaging sensor according to Claim 3, wherein the intermediate buffer is provided off-chip from the sensor array.

5. An imaging sensor according to Claim 1, further comprising a respective plurality of processors, each processor coupled to a respective one of the output pipelines, and wherein said means for duplicating comprises a communication link between processors that border the overlap region, and wherein duplicate pixels are communicated between processors over the communication link.

6. A method of operating an imaging sensor which includes a sensor array segmented into plural disjoint segments including at least a first segment and a second segment separated by a boundary, the method comprising the step of duplicating image data for an overlap region at the boundary.

7. A method according to Claim 6, wherein said step of duplicating image data further comprises the steps of:

storing charges or voltages from a non-overlap region of the first segment into a output pipeline;

storing charges or voltages from the overlap region of the first segment and the second segment into the output pipeline; and  
providing charges or voltages from the output pipeline to a processor.

8. A method according to Claim 6, wherein said step of duplicating image data further comprises the steps of:

storing charges or voltages from a non-overlap region of the first segment into a pipeline;  
sending charges or voltages from the overlap region of the first segment and the second segment to a shift out line;  
storing charges or voltages from the shift out line to an intermediate buffer; and  
providing charges or voltages from the pipeline and from the intermediate buffer to a processor.

9. A method according to Claim 8, wherein the intermediate buffer is provided off-chip from the sensor array.

10. A method according to claim 6, wherein said step of duplicating image data further comprises the steps of:

storing charges or voltages from the first segment into a pipeline;  
providing charges or voltages from the pipeline to a first processor, the first processor for processing pixel data for the first segment; and  
communicating pixel data for the overlap region between the first processor and a second processor, the second processor for processing pixel data for the second segment.

45

50

55



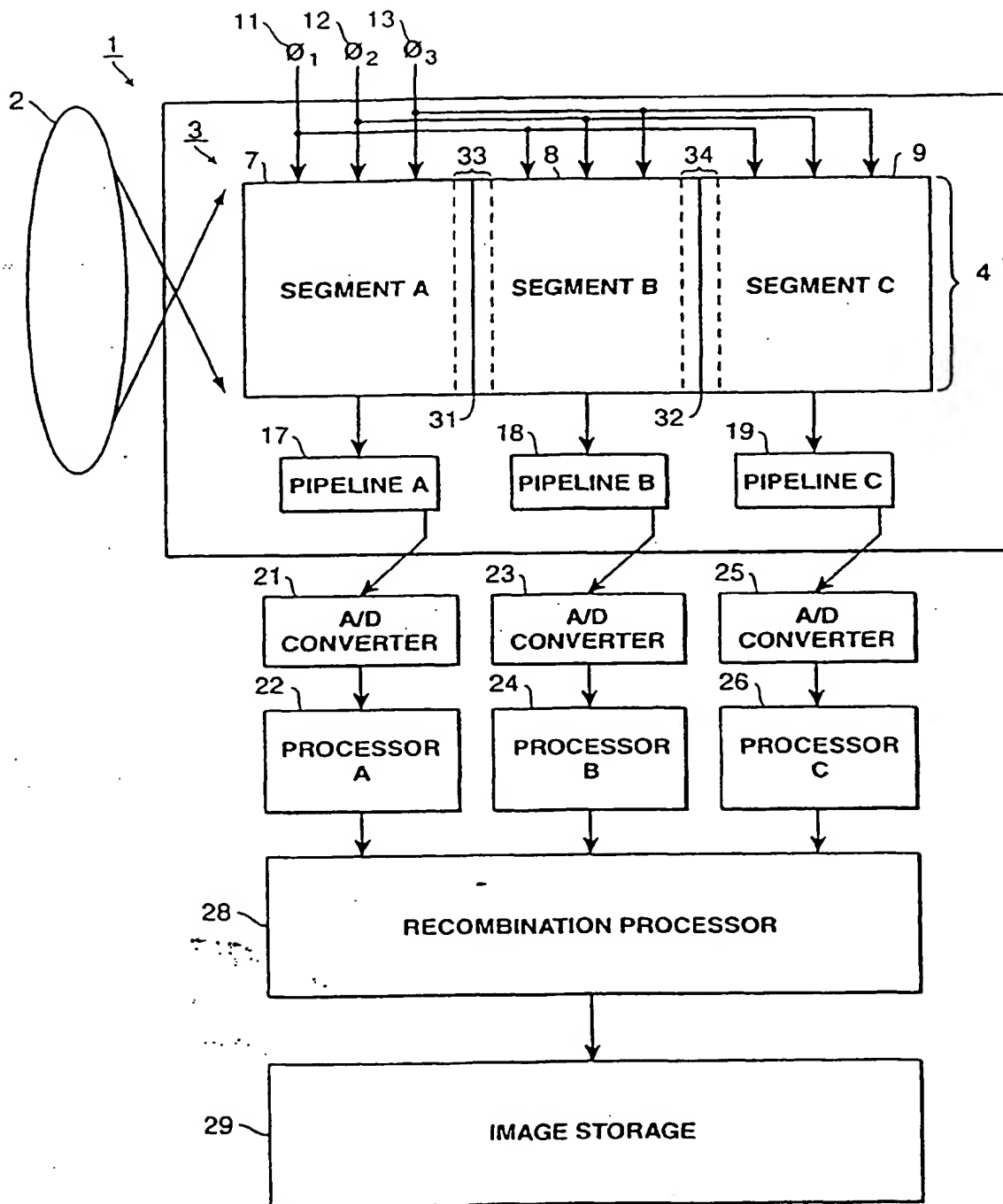


FIG. 1

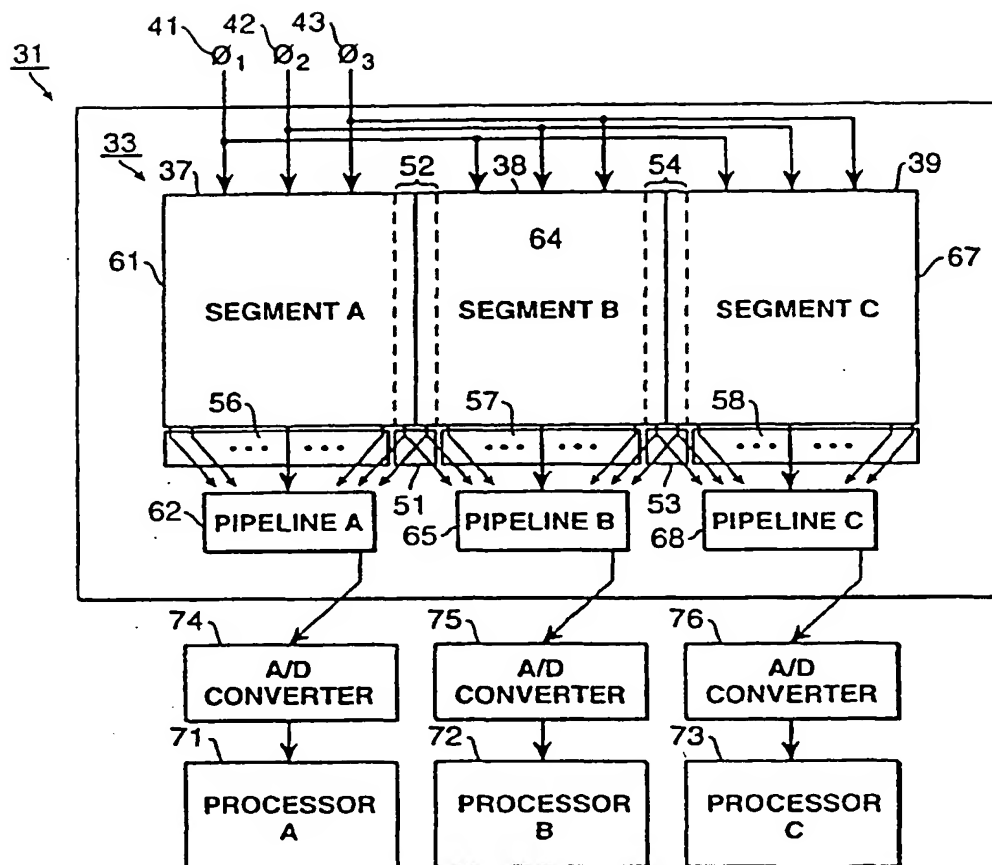


FIG. 2

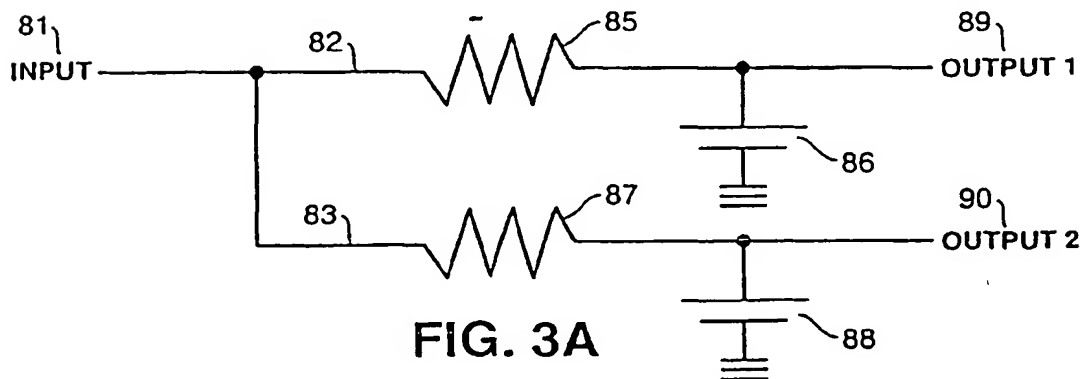


FIG. 3A

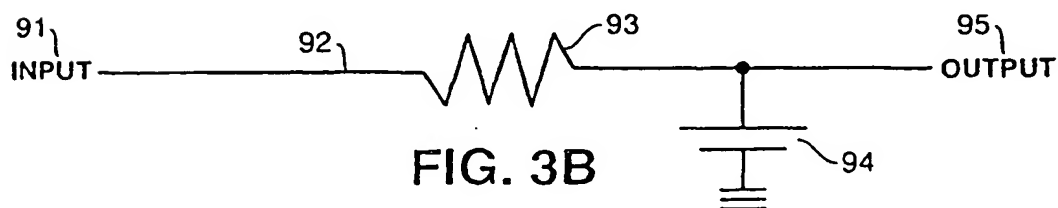


FIG. 3B

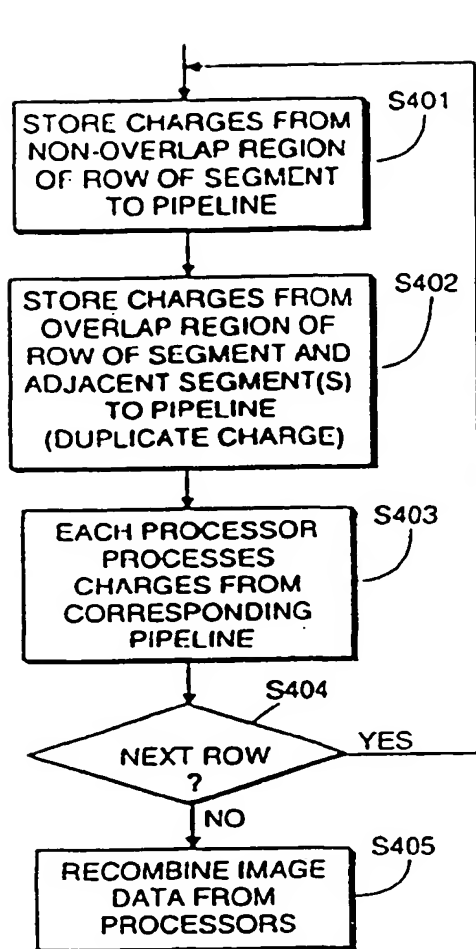


FIG. 4

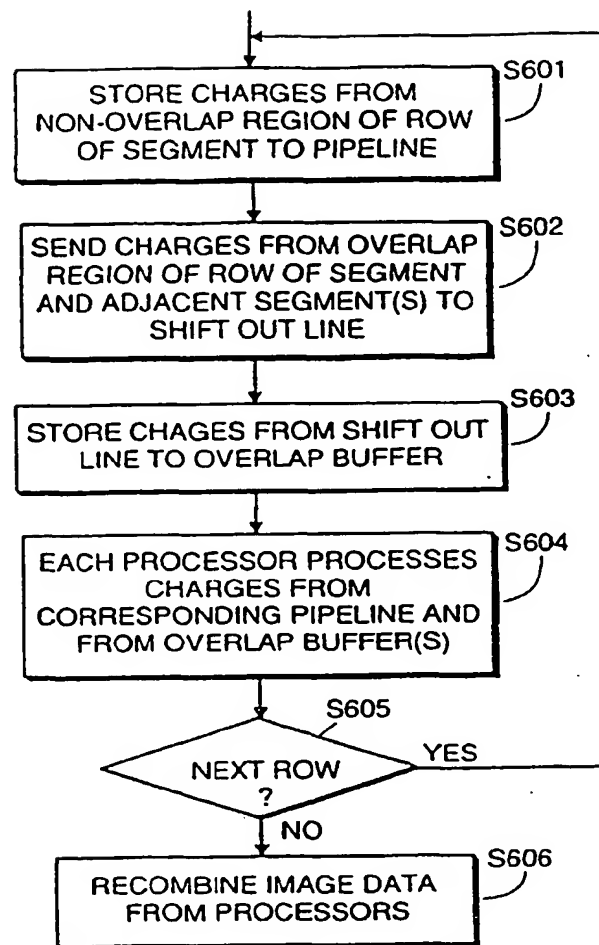


FIG. 6

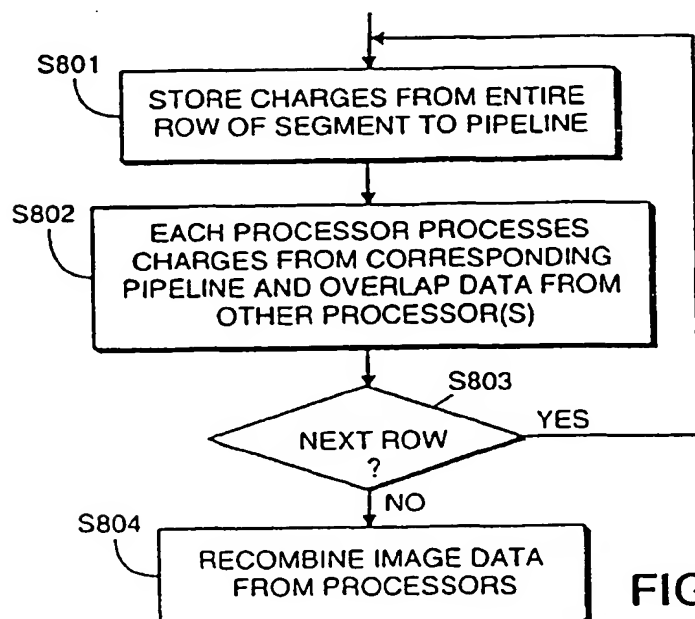


FIG. 8

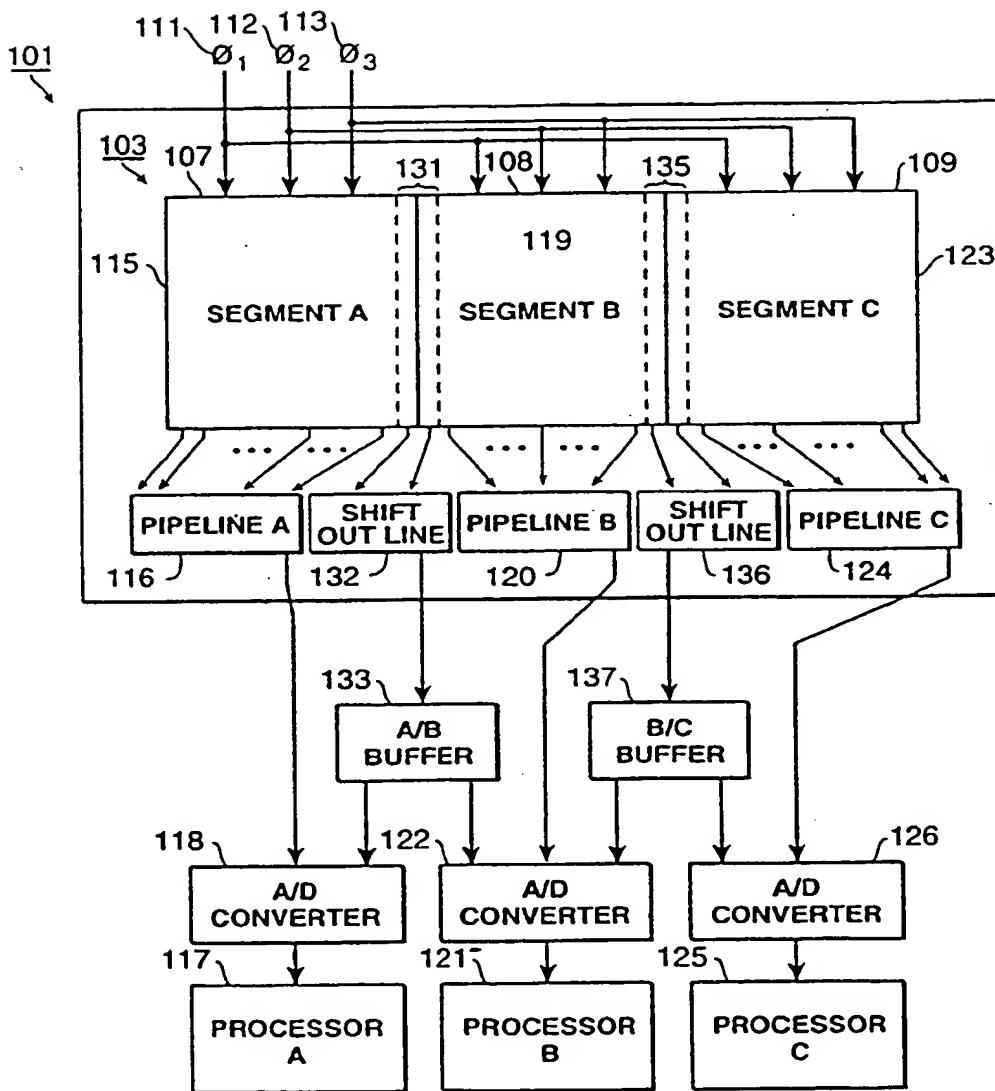


FIG. 5

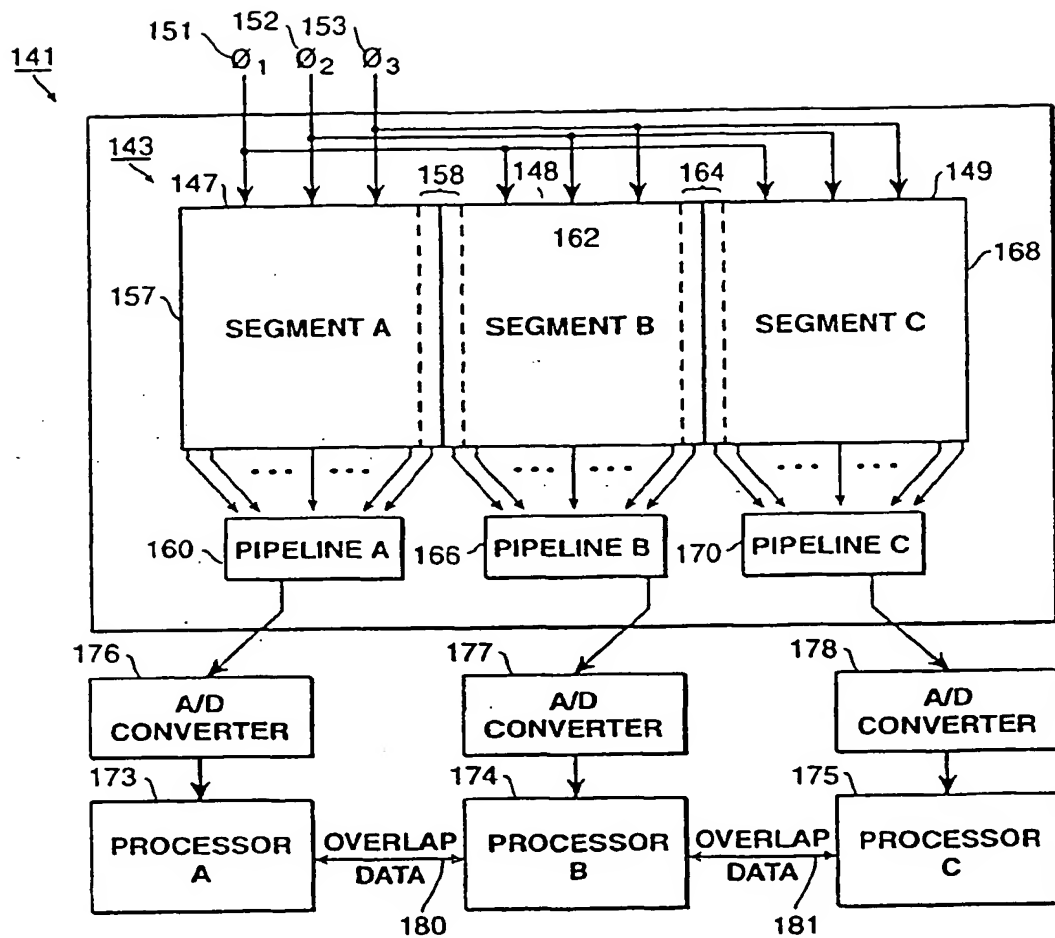


FIG. 7



European Patent  
Office

## EUROPEAN SEARCH REPORT

Application Number  
EP 00 30 1689

DOCUMENTS CONSIDERED TO BE RELEVANT			
Category	Citation of document with indication, where appropriate, of relevant passages	Relevant to claim	CLASSIFICATION OF THE APPLICATION (Int.Cl.7)
X	WO 94 18789 A (POLYCOM INC) 18 August 1994 (1994-08-18) * page 7, line 25 - page 8, line 2 * ---	1,6	H04N3/15 H04N5/217
A	EP 0 866 502 A (DALSA INC) 23 September 1998 (1998-09-23) * column 2, line 46 - column 3, line 23; figure 10 * ---	1,6	
A	US 5 220 626 A (MONMA HISAYOSHI ET AL) 15 June 1993 (1993-06-15) * column 3, line 25 - line 32 * -----	1,6	
			TECHNICAL FIELDS SEARCHED (Int.Cl.7)
			H04N
The present search report has been drawn up for all claims			
Place of search THE HAGUE		Date of completion of the search 22 June 2000	Examiner Bequet, T
CATEGORY OF CITED DOCUMENTS X : particularly relevant if taken alone Y : particularly relevant if combined with another document of the same category A : technological background O : non-written disclosure P : intermediate document T : theory or principle underlying the invention E : earlier patent document, but published on, or after the filing date D : document cited in the application L : document cited for other reasons & : member of the same patent family, corresponding document			

EPO FORM 1503 03 82 (P04C01)

**ANNEX TO THE EUROPEAN SEARCH REPORT  
ON EUROPEAN PATENT APPLICATION NO.**

EP 00 30 1689

This annex lists the patent family members relating to the patent documents cited in the above-mentioned European search report.  
The members are as contained in the European Patent Office EDP file on  
The European Patent Office is in no way liable for these particulars which are merely given for the purpose of information.

22-06-2000

Patent document cited in search report		Publication date	Patent family member(s)		Publication date
WO 9418789	A	18-08-1994	NONE		
EP 0866502	A	23-09-1998	JP 11027589	A	29-01-1999
US 5220626	A	15-06-1993	JP 2522700	B	07-08-1996
			JP 3024857	A	01-02-1991

EPO FORM P0459

For more details about this annex : see Official Journal of the European Patent Office, No 12/82